



2

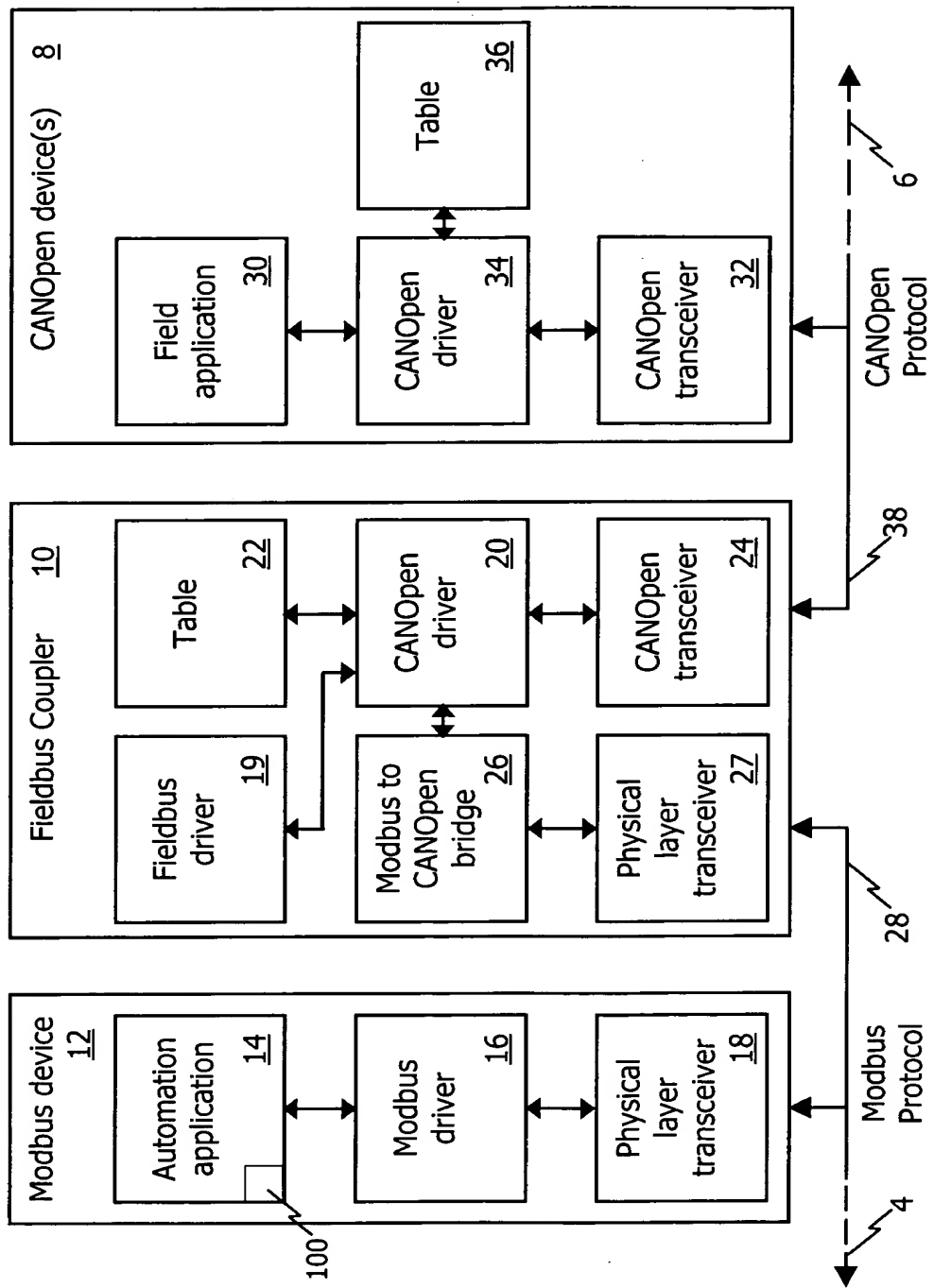


Fig. 1



42 ↘

<b>Data</b>	<b><u>46</u></b>
<b>Function Code</b>	<b><u>40</u></b>
<b>Device ID</b>	<b><u>44</u></b>

***Fig. 2A***

40 ↘

<b>Error Check</b>	<b><u>74</u></b>
<b>Read/Write Data</b>	<b><u>70</u></b>
<b>Number Low Bytes</b>	<b><u>68</u></b>
<b>Number High Bytes</b>	<b><u>66</u></b>
<b>Starting Low Address</b>	<b><u>64</u></b>
<b>Starting High Address</b>	<b><u>62</u></b>
<b>Sub-Index</b>	<b><u>60</u></b>
<b>Index Low</b>	<b><u>58</u></b>
<b>Index High</b>	<b><u>56</u></b>
<b>Node ID</b>	<b><u>54</u></b>
<b>Extend Bit</b>	<b><u>52</u></b>
<b>Reference Type</b>	<b><u>50</u></b>
<b>Function Code "43"</b>	
<b>Slave ID</b>	<b><u>72</u></b>

***Fig. 2B***



140 ↘

<b>Error Check</b>	<b><u>174</u></b>
* * * <b>Next Function Code</b> * * *	
* * * <b>Next Function Code</b> * * *	
<b>Read/Write Data</b>	<b><u>170</u></b>
<b>Number Low Bytes</b>	<b><u>168</u></b>
<b>Number High Bytes</b>	<b><u>166</u></b>
<b>Starting Low Address</b>	<b><u>164</u></b>
<b>Starting High Address</b>	<b><u>162</u></b>
<b>Sub-Index</b>	<b><u>160</u></b>
<b>Index Low</b>	<b><u>158</u></b>
<b>Index High</b>	<b><u>156</u></b>
<b>Node ID</b>	<b><u>154</u></b>
<b>Extend Bit</b>	<b><u>152</u></b>
<b>Reference Type</b>	<b><u>150</u></b>
<b>Function Code</b>	
<b>Reserved Byte</b>	
<b>Function Code "41"</b>	
<b>Slave ID</b>	<b><u>172</u></b>

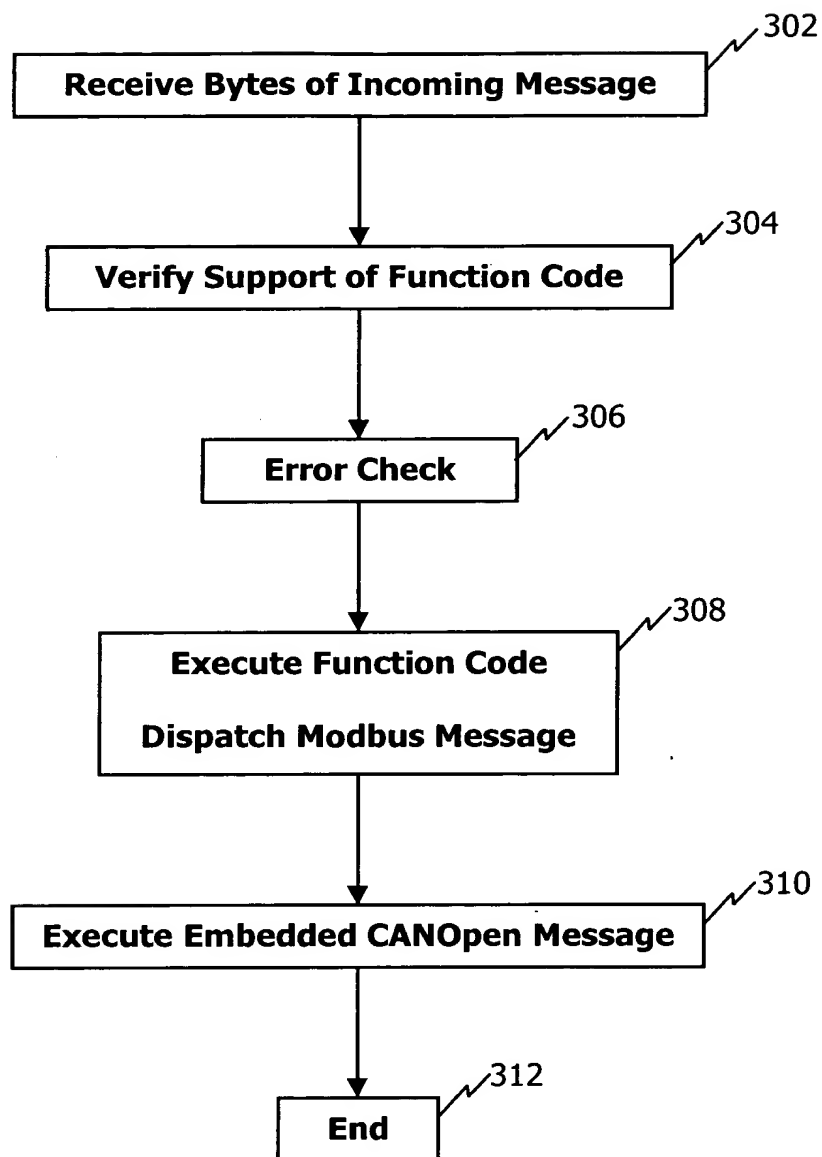
***Fig. 2C***



240 ↘

<b>Error Check</b>	<b><u>274</u></b>
* * * <b>Next Function Code</b> * * *	
* * * * * * *	
* * * <b>Next Function Code</b> * * *	
<b>Slave ID</b>	<b><u>272</u></b>

***Fig. 2D***



**Fig. 3**



Modbus function code	Sub- function or sub-index	Command
3		Read 4x registers
4		Read 3x registers
16		Write 4x registers
22		Mask write
23		Combination 4x read/write
43		Read Object Dictionary entries
43		Write Object Dictionary entries
43	1	COMS-Reset_req
43	2	Start_BootUpAuto_req
43	3	Command not used
43	4	Run_Network_req
43	5	Stop_Network_req
43	6	Store_Config_req
43	7	Store_Config_Sim_req
43	8	Restore_Config_req
43	9	Request mastery over PI output data for AI-config-tool
43	10	Release mastery over PI output data for AI-config-tool
43	11	Request mastery over application parameter area for AI-config-tool
43	12	Release mastery over application parameter area for AI-config-tool
43	13	Save password for access via config port
43	14	Set FBC into protected mode
43	15	Set FBC into edit mode (= leave protected mode)
125		Flash programming commands
125	1	Read hardware identification
125	2	Not supported
125	3	Not supported
125	4	Confirm mode
125	5	Enter kernel mode
125	6	Exit kernel mode
125	7	Fill flash memory
125	8	Program flash memory
125	9	Read flash memory
126		Programming commands
126	1	Stop
126	2	Start

**Fig. 4**